

Statement of Purpose in STEM #1

My academic objective is to pursue graduate study in electrical engineering with focus on signal processing and embedded systems. This interest developed through sustained coursework, laboratory training, and applied technical projects that emphasized system-level thinking, mathematical modeling, and hardware-software integration. Graduate study represents a necessary step in deepening both theoretical understanding and design capability within this field.

I completed a Bachelor of Science in Electrical Engineering at Northern Technical University. Core coursework included circuits, digital systems, signals and systems, probability theory, and control systems. These courses emphasized analytical rigor and required translating mathematical concepts into functional system models. Laboratory components reinforced theoretical learning by introducing real-world constraints such as noise, latency, and hardware limitations. Through this foundation, I developed a strong interest in how signals behave under constrained computational and physical environments.

My academic preparation includes multiple project-based experiences focused on signal analysis and embedded design. During my junior year, I completed a laboratory project involving digital filtering using MATLAB. The project required designing low-pass and band-pass filters, evaluating frequency responses, and testing performance under varying noise conditions. I analyzed tradeoffs between accuracy and computational efficiency and documented results in a formal technical report. This experience strengthened my interest in algorithm optimization and system evaluation.

In my senior year, I worked on a capstone project centered on embedded sensor systems for environmental monitoring. The project involved integrating sensors with microcontrollers, developing firmware for real-time data acquisition, and implementing signal conditioning techniques. I was responsible for firmware development and signal processing logic. This work required balancing power consumption, processing speed, and reliability. Through this project, I gained hands-on experience in embedded system architecture and reinforced my interest in resource-constrained signal processing applications.

Beyond coursework, I completed a summer research assistantship in a university laboratory focused on adaptive signal processing. My responsibilities included data preprocessing, algorithm testing, and performance evaluation across multiple datasets. I assisted in comparing adaptive filtering techniques under different signal conditions and contributed to internal documentation. Exposure to research methodology strengthened my ability to work independently and evaluate results critically. It also introduced me to the iterative nature of technical research and the importance of reproducibility.



The Master's program in Electrical Engineering at Horizon Institute aligns closely with my academic goals. The curriculum's emphasis on signal processing theory, embedded systems, and advanced mathematics supports my technical development. Faculty research in real-time signal analysis and adaptive systems reflects the academic environment I seek. Access to specialized laboratories and project-based coursework provides opportunities to connect theory with applied research.

During graduate study, my short-term goals include advancing proficiency in signal analysis techniques, strengthening embedded system design skills, and contributing to applied research initiatives. I intend to pursue a thesis focused on signal optimization within constrained systems. Long-term, I aim to work in advanced system design or applied research roles within the technology sector, where rigorous engineering decisions shape reliable products.

Graduate training offers the academic depth and technical rigor required for sustained growth in electrical engineering. My academic background, laboratory experience, and defined technical interests demonstrate readiness for advanced study and commitment to meaningful contribution within the field.