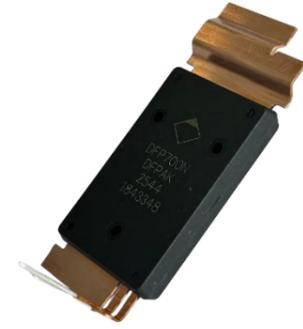


DFP500N120G1

DFPAK High Power Module

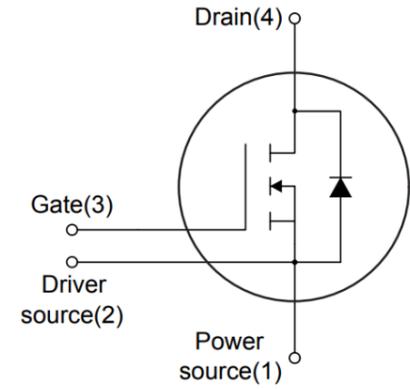
Features and Benefits

Advanced SiC MOSFET Technology
 Best-in-class $R_{th} < 0.059 \text{ } ^\circ\text{C/W}$ and dielectric strength $> 4.2\text{kV}$ isolation)
 Very high current capability (500A) with 4 high performance SiC dies.
 $V_{ds} = 1200\text{V}$, $I_{ds} = 500\text{A}$, $R_{ds(on)} = 2.67\text{m}\Omega$ Typical
 50% system-level BOM reduction with highly integrated package.
 Low $R_{DS(on)}$ over the entire temperature range
 High speed switching performances
 Source sensing pin for increased efficiency
 Fast and robust intrinsic body diode
 High operating junction temperature capability (TBD)
 Full Square SOA
 RoHS Lead free
 Complaint to AEC-Q100 Automotive Qualification standard.



Applications

Inverter for BEV, HEV & FCEV
 BLDC Motor Power Stage
 High Power Charging Systems
 AC/DC Converters
 DC/AC Converters
 DC/DC Switch Mode Power Supply



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	VDS	1200	V
Gate-Source Voltage	VGS	-10/+22	V

RECOMMENDED OPERATING CONDITIONS

Gate-Source Voltage	Vgs	-3/+18	V
Operating Junction Temperature	TJ	-55 to 175	$^\circ\text{C}$
Extended Operating Junction Temperature (200h max.)	TJ,ext	-55 to 200	$^\circ\text{C}$
Storage Temperature	Tstg	-55 to 125	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ORDERING INFORMATION

Device	DFP500N120G1
Package Type	DFPAK
Shipping	TBD

Description

The DFP500N120G1 has four silicon carbide power MOSFET devices featuring high-current capability and high-breakdown voltage. Best in class dielectric and thermal path conductivity is ensured by Single Crystal Diamond (SCD) for optimal performance and reduced junction temperature. Direct sintering of the power switches achieves remarkable electrical and thermal conductivity to the substrate, enhanced reliability and exceptional stress tolerance. The device features a very low $R_{DS(on)}$ over the entire temperature range combined with low capacitances and high switching operations to improve performance in energy efficiency, system size and weight reduction.

Electrical Ratings

Table 1. Absolute Maximum Ratings

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit	Comments
$V_{(BR)DSS}$		$V_{gs} = 0V$ $I_d = 1mA$	1200			V	$[-55^{\circ}C \leq T_j \leq 175^{\circ}C]$ Over whole temperature range
V_{GS}	Gate-source voltage (Maximum)		-10		22	V	
	Gate-source voltage (recommended operating values)		-3		18	V	
I_d	Drain current (continuous)	$V_{gs} = 18V$ $V_{ds} = 2V$			350	A	indicated chip capability, depending on thermal properties of assembly (assumption: $T_j=175^{\circ}C$.)
	Drain current (continuous)	$V_{gs} = 18V$ $V_{ds} = 2V$			500	A	indicated chip capability, depending on thermal properties of assembly (assumption: $T_j=25^{\circ}C$)
I_{DM}	Drain current (pulsed)				TBD	A	
P_{TOT}	Total power dissipation				TBD	W	
I_F	Diode forward current (continuous)	$V_{gs} = -3V$ $V_{ds} = -4V$			200	A	indicated chip capability, depending on thermal properties of assembly (assumption: $T_j=175^{\circ}C$)
	Diode forward current (continuous)	$V_{gs} = -3V$ $V_{ds} = -4V$			280	A	indicated chip capability, depending on thermal properties of assembly (assumption: $T_j=25^{\circ}C$)
$I_{F(Pulsed)}$	Pulsed peak body diode current, limited by T_{jmax}	$V_{gs} = -3V$			TBD	A	
t_{sc}	Short circuit withstand time	$V_{gs,on} = 18V$; $V_{gs,off} = -3V$; $V_{ds} = 850V$, $T_j = 25^{\circ}C$, $dI/dt < 10kA/\mu s$		TBD		μs	

E_{sc}	Short circuit withstand energy	$V_{gs,on} = 18\text{ V};$ $V_{gs,off} = -3\text{ V},$ $V_{ds} = 850\text{ V}, T_j = 25^\circ\text{C},$ $di/dt < 10\text{ kA}/\mu\text{s}$		TBD		J	
T_j and T_{ext}	Operating Junction Temperature		-55		175	$^\circ\text{C}$	
	Extended Operating Junction Temperature (200h max.)		-55		200	$^\circ\text{C}$	

Table 2. Thermal Data

Symbol	Parameter	Min.	Typ.	Max.	Unit	Comments
T_{stg}	Storage Temperature	-55		200	$^\circ\text{C}$	
R_{thJC}	Thermal resistance, junction-to-case		0.059		$^\circ\text{C}/\text{W}$	Based on total power dissipation of 600W

Electrical Characteristics

Table 3. Static Characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit	Comments
$R_{DS(on)}$	Drain-source on-resistance	$V_{gs} = 18\text{ V}, I_d = 400\text{ A}$ $T_j = 25^\circ\text{C}$		2.67	3.75	$\text{m}\Omega$	
	Drain-source on-resistance	$V_{gs} = 18\text{ V}, I_d = 400\text{ A}$ $T_j = 175^\circ\text{C}$		5.25		$\text{m}\Omega$	
$V_{GS(th)}$	Gate-Source threshold voltage	$I_d = 33\text{ mA}, V_{gs} = V_{ds}$ $T_j = 25^\circ\text{C}$	2.08	3.34	4.63	V	
	Gate-Source threshold voltage	$I_d = 33\text{ mA}, V_{gs} = V_{ds}$ $T_j = 175^\circ\text{C}$		2.6		V	
I_{dss}	Zero gate voltage drain current	$V_{ds} = 1200\text{V}, V_{gs} = 0\text{ V},$ $T_j = 25^\circ\text{C}$			100	μA	
	Zero gate voltage drain current	$V_{ds} = 1200\text{V}, V_{gs} = 0\text{ V},$ $T_j = 175^\circ\text{C}$			1000	μA	
I_{gss}	Gate-Source leakage current	$V_{ds} = 0\text{V}, V_{gs} = -10\text{ V}$ $T_j = 25^\circ\text{C}$			400	nA	
	Gate-Source leakage current	$V_{ds} = 0\text{V}, V_{gs} = 22\text{ V}$ $T_j = 25^\circ\text{C}$			400	nA	
V_{sd}	Diode forward voltage	$V_{GS} = -3\text{ V}, I_D = 400\text{ A}$ $T_J = 25\text{C}$	3.3	4.75	6.2	V	

	Diode forward voltage	VGS = -3 V, ID = 540 A TJ = 25C	-	5.3	-	V	
	Diode forward voltage	VGS = -3 V, ID = 540 A TJ = 175C	-	4.9	-	V	

Table 4. Dynamic Characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C _{iss}	Input capacitance	VDS = 800 V, VGS = 0 V, f = 1 MHz		24000		nF
C _{oss}	Output capacitance	VDS = 800 V VGS = 0 V, f = 1 MHz		880		pF
C _{rss}	Reverse transfer capacitance	VDS = 800 V VGS = 0 V, f = 1 MHz		20		pF
R _{gint}	Total internal gate resistance	f = 1 MHz	1.58	2.3	2.95	Ω
Q _g	Total gate charge	VDD = 800 V ID = 135 A Vgs = -3/18 V		816	940	nC
Q _{gs}	Gate-source charge	TBD		TBD		nC
Q _{gd}	Gate-drain charge	TBD		TBD		nC

Table 5. Switching Energy (Inductive Load)

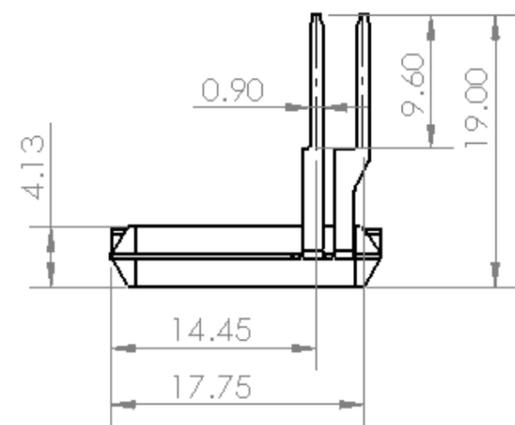
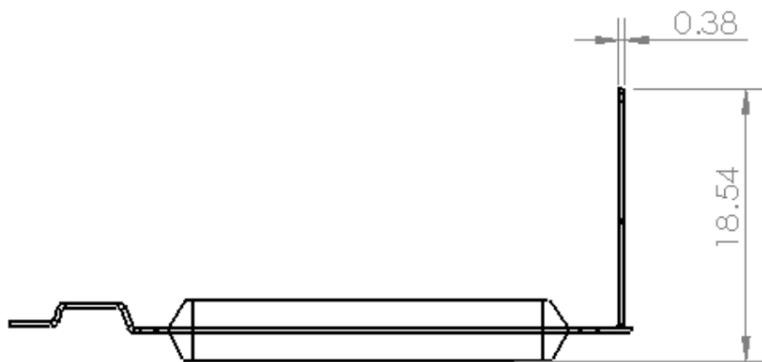
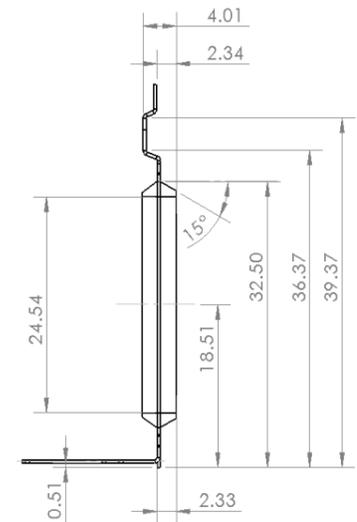
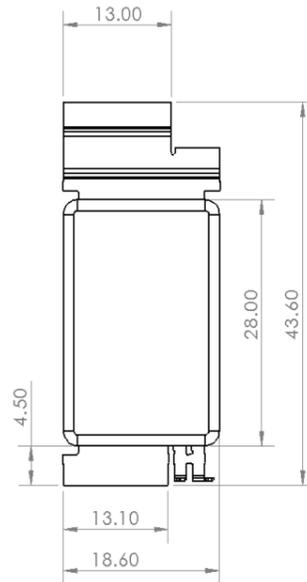
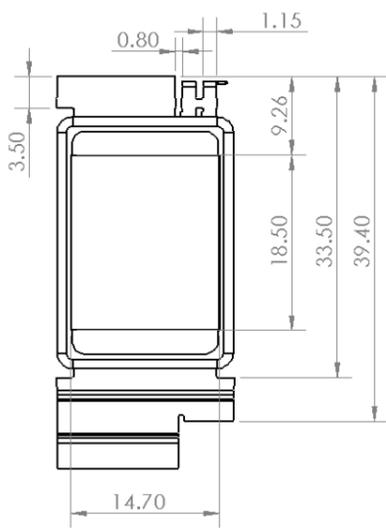
Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
E _{on}	Turn-on switching energy	VGS = -3/18 V ID = 540 A VDD = 800 V Rg,(on,off) = 0.5		10.24		mJ
E _{off}	Turn-off switching energy	VGS = -3/18 V ID = 540 A VDD = 800 V Rg,(on,off) = 0.5		7.16		mJ

Table 6. Reverse SiC diode characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
T _{rr}	Reverse recovery time	VGS = -3 V IS = 400 A, VDS = 800 V, di/dt = 1000 A/s, TJ = 25C		TBD		ns
		VGS = -3 V IS = 400 A, VDS = 800 V, di/dt = 1000 A/s, TJ = 175C				
Q _{rr}	Reverse recovery charge	VGS = -3 V IS = 400A, VDS = 800 V di/dt = 1000 A/s, TJ = 25C VGS = -3 V IS = 400A, VDS = 800 V di/dt = 1000 A/s, TJ = 175C		TBD		nC

I _{RRM}	Reverse recovery current	VGS = -3 V IS = 400A, VDS = 800 V di/dt = 1000 A/s, TJ = 25C	TBD	A
		VGS = -3 V IS = 400A, VDS = 800 V di/dt = 1000 A/s, TJ = 175C		
		VGS = -3 V, IS = 400A, VDS = 800 V di/dt = 1000 A/s, TJ = 175C		

DFPAK Package Outline



Note: All dimensions expressed in millimeters