



Agile for Hardware



Authoring

Manage requirements & verifications, create MBSE models, generate trace matrices, and track changes.



Infrastructure

Integrate requirements and system models throughout your technology stack using CI/CD pipelines, our Python ORM, or the SysML v2 compliant API.



Automation

Fully-offline AI/ML capability processes disparate engineering artifacts into shareable system models.

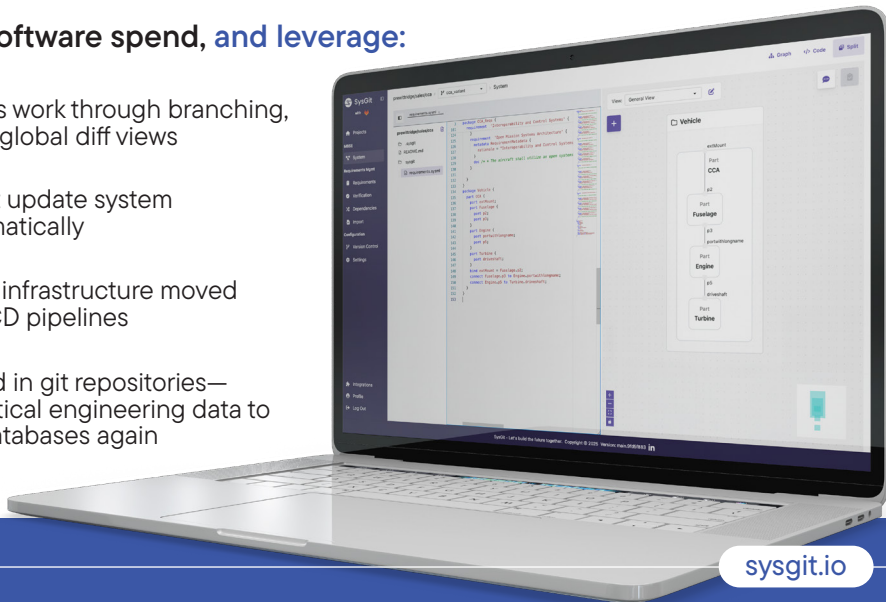
Iterate Hardware **at Software Velocity.**

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Agile Infrastructure for Systems Engineering

Consolidate software spend, and leverage:

- + Asynchronous work through branching, merging, and global diff views
- + AI agents that update system models automatically
- + Digital thread infrastructure moved into your CI/CD pipelines
- + All data stored in git repositories—never lose critical engineering data to proprietary databases again



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