

Features

3000A, 4500V
40kA Pulse Current Capability
Fast Turn-off Time
Light Weight Package

Description

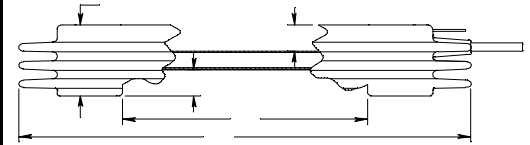
The SPT312 reverse blocking thyristor is suitable for inverter applications up to 200Hz. The silicon junction is manufactured by the proven multi-diffusion process and utilizes the exclusive eight (8) arms involute gate structure for lower switching losses.

The design utilizes the revolutionary "Light Silicon Sandwich" or LSS technology, a new termination technique which eliminates heavy refractory metal as a substrate but still employs the alloyed anode interface necessary for high surge current duty. The light weight plastic package allows the insertion of liquid cooled chillers closer to the silicon junction. Copper inserts can be supplied for adjoining commercially available flat surfaced heat dissipators.

Package

A=5.375in, B=3.204in, C=0.792in, D=0.2902in

Notes - 1, 2 & 3


MODEL RATING AVAILABILITY

PART NUMBER	V _{DRM}	V _{RRM}
SPT312HK	4500	4500
SPT312HH	4400	4400
SPT312HF	4300	4300
SPT312HD	4200	4200
SPT312HB	4100	4100
SPT312FT	4000	4000

Limiting Characteristics and Ratings

At T_J = 125 °C, Unless Otherwise Specified

Average On-State Current (T_C=70

For Fusing (8.3ms / 10ms)

Critical Gate Trigger Voltage (V_D = 12V, T_J = 25

Critical Gate Trigger Current (V_D = 12V, T_J = 25

Non-Trigger Gate Voltage (V_D = 2000V

Non-Trigger Gate Current (V_D

Turn-Off Time (5A/μs, >100V, 400V/μ

Turn-On Delay (V_D = 50%V_{DRM}

Rate of Change of Voltage (V_D=70% V_{DRM}

Rate of Change of Current (V_D=50% V_{DRM}

V _{DRM}	4500	V
V _{RRM}	4500	V
I _{T(AV)}	3000	A
I _{TSM}	40 / 37.5	kA
I t	6.6 / 7	MA s
V _{GT}	4.5	V
I _{GT}	300	mA
V _{GD}	0.8	V
I _{GD}	15	mA
V _{OC}	50	V
I _{SS}	5	A
10 μs duration / 0.5 μs rise time		
T _{off}	400	s
t _d	4	
dv/dt	1000	V/ s
di/dt	300	A/ s
T _J , T _{STG}	0 to +125	°C
F	13000-16000	lbs

Notes

- Optional external posts dwg. # 0215B8331; Ni plated copper, 0.35" thick each.
- Compressed thickness including external posts is 0.88" - 0.89" (22.35mm - 22.61mm).
- Weight 14 oz., 2.7 lbs with posts.

Electrical Specifications

At T_J = 125°C, Unless Otherwise Specified

PARAMETERS	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Peak Off State Blocking	I _{DRM}	V _D = 80%V _{DRM}			450	mA
Forward & Reverse Current	I _{RRM}				350	mA
On State Voltage	V _{TM}	I _T = 4kA Pulse			2.0	V
Max. Peak Recovery Current	I _{RM}	di/dt = 10A/ s Snap. S = .5-.33			310	A
Thermal Resistance	R _{JC}	Double Side Cooling			0.0049	C/W

Typical Performance Curves

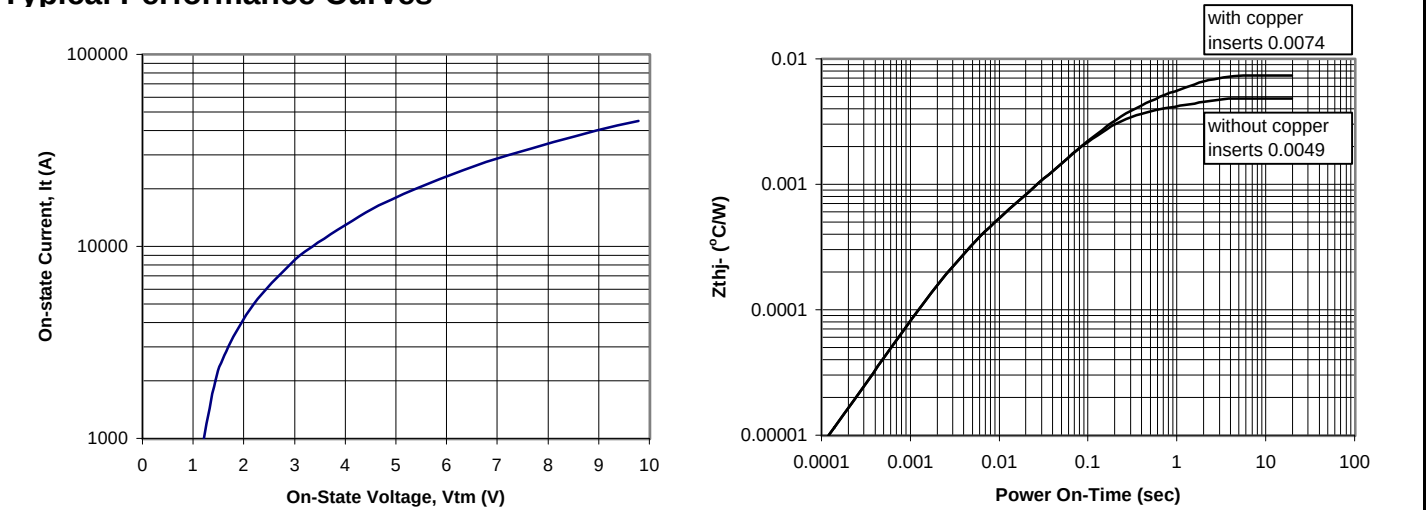


FIGURE 1. ON-STATE CURRENT vs ON-STATE VOLTAGE

FIGURE 2. THERMAL IMPEDANCE vs POWER ON TIME

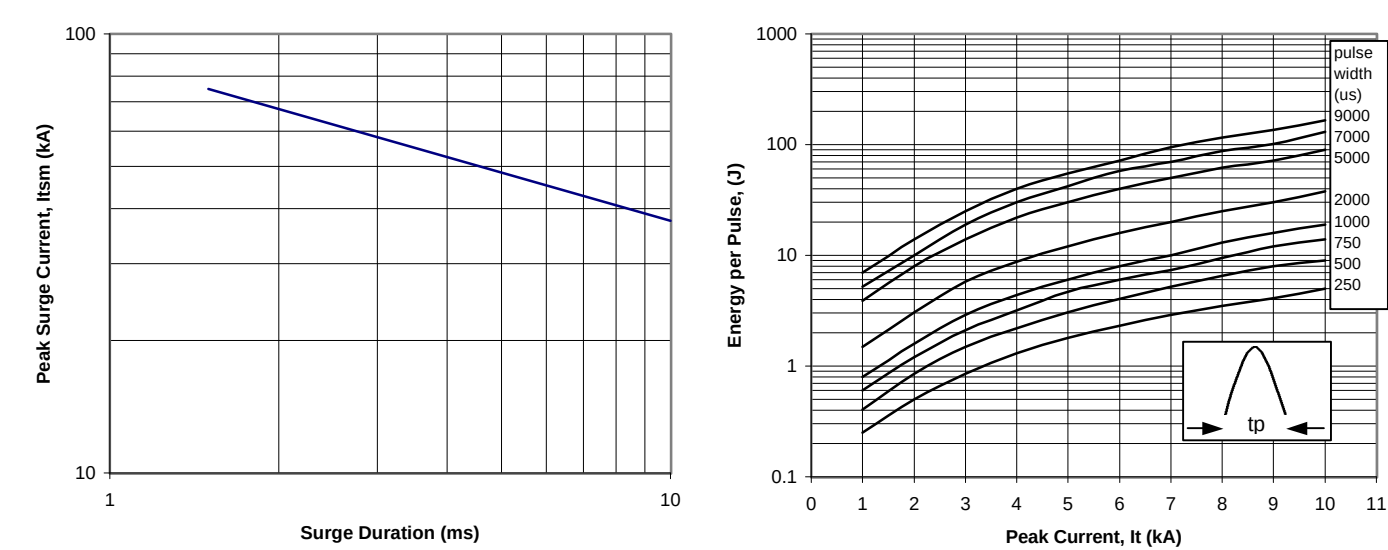


FIGURE 3. PEAK I_{TSM} vs PULSE DURATION FOR HALF SINE CURRENT

FIGURE 4. ON-STATE ENERGY vs ON-STATE CURRENT

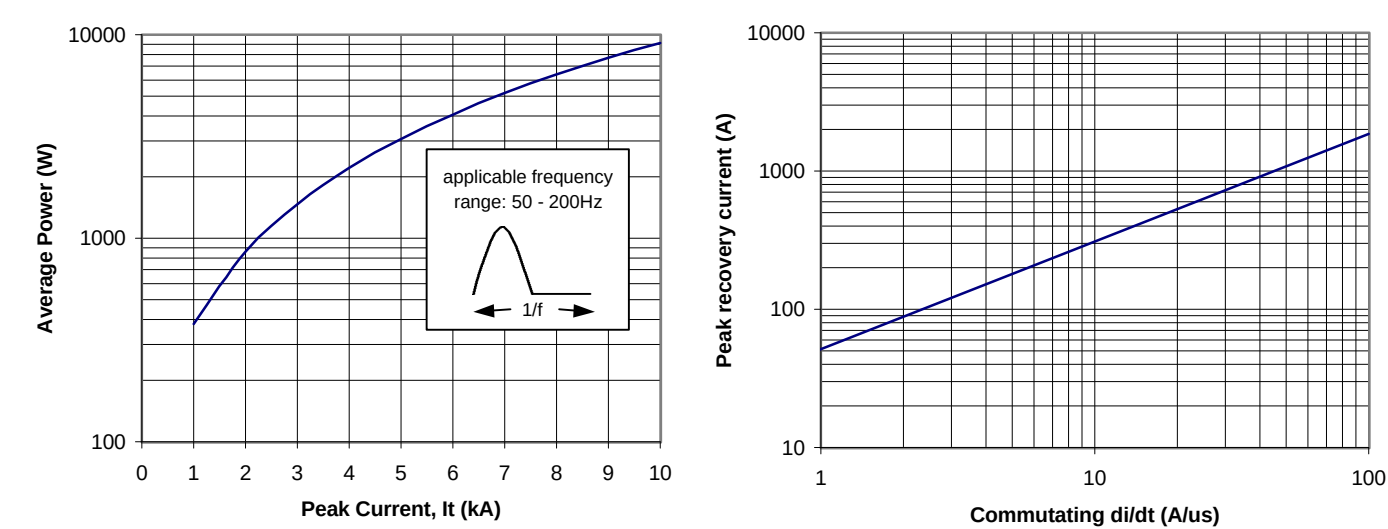


FIGURE 5. AVERAGE FULL CYCLE POWER LOSS vs ON-STATE CURRENT

FIGURE 6. PEAK I_{REC} vs COMMUTATING di/dt