

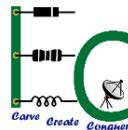


THE NATIONAL INSTITUTE OF ENGINEERING

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Manandavadi Road, Mysuru-570 008

Phone: 0821-2480475, 2481220, 4004944 Fax: 0821-2485802, Email: icdesignclub@nie.ac.in



Dated: 25/ 03 /2025

“DECCTEROUS”- Integrated Circuit Design Club

The DECCTEROUS IC Design Club is a student-driven, faculty-mentored initiative dedicated to advancing knowledge in semiconductor technology, IC design methodologies, embedded systems, programming, AI-ML, and more. This student club is officially inaugurated on **6th July 2024** under the Department of Electronics and Communication Engineering, NE Mysore. Serving as a dynamic platform, DECCTEROUS empowers students with technical expertise, hands-on project experience, and insights into industry advancements while strengthening fundamental concepts. Over the past nine months, the club has actively conducted workshops, training sessions, guest lectures, design challenges, technical competitions, women empowerment programs, industrial visits, collaborative projects with industry experts, RISC-V programs, and more. These activities ensure students remain engaged with cutting-edge technologies and gain invaluable practical exposure.

The DECCTEROUS IC Design Club has played a vital role in shaping future engineers, equipping them with skills in circuit design, system-level integration, debugging, PCB design, and embedded programming. Many members have secured prestigious internships, excelled in hackathons, presented research papers, undertaken industry-sponsored projects, and emerged as winners in technical competitions—all under the guidance of dedicated faculty Co-ordinators of ICDC- **Dr. Salila Hedge and Dr. Remya Jayachandran**.

Our mission is to equip students with cutting-edge skills and industry insights, preparing them to become future leaders in the field of engineering. Whether you're a beginner or an advanced learner, this student club offers a wealth of opportunities to explore, create, and innovate.

The envisioned Integrated Circuit Design Club aims to achieve the following objectives:

1. Providing a forum for students and faculty members to engage in interactive discussions, workshops, and knowledge-sharing sessions related to circuit design principles, methodologies, and emerging technologies.
2. Providing Strong fundamentals in the IC design that enables the students to get placed in the Core companies
3. Foster a collaborative environment that encourages hands-on projects, design competitions, and practical applications of circuit design theories.
4. Cultivate a network of like-minded individuals, encompassing students, researchers, and industry professionals, to facilitate mentorship, career guidance, and collaboration opportunities.

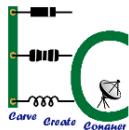


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5. Enhance the visibility and reputation of our department through participation in circuit design competitions, industry partnerships, and knowledge dissemination.

This club aligns with our commitment to providing our students with practical and real-world knowledge to complement their academic learning.

Mission:

The mission of the Circuit Design Club is to create an environment for experiential learning where every member gets an opportunity to acquire & develop the skills needed for pursuing a career in any Electronics and Communication Domain.

Vision:

To become an active centre of excellence in both analog and digital circuit design for various applications that can cater to the needs of the booming industry by bridging the gap between industry & academics.

Website: DECCTEROUS IC Design Club: <https://deccteroous.in/>

ICDC Faculty Co-ordinators:

Main Coordinator:

1. **Dr. Remya Jayachandran, Assistant Professor & Deputy IQAC Head, ECED**

Faculty Advisor:

Dr. Ashok K, Associate Professor, ECED

AICTE IDEALAB TechGuru

1. **Prof. Nitesh K, Assistant Professor ECED**

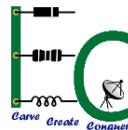


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ADVISORY COMMITTEE

INDUSTRY

1. Dr. Supriya Unnikrishnan, Principle Engineer, Ignitarium
2. Dr. Nisha Haridas, Senior Engineer, Qualcomm
3. Mr. Vijayakrishnan K K, Senior Staff Engineer, Infineon Technologies
4. Mr. Edet Bijoy K, Digital IC Design Engineer, KaikuTek, Taiwan
5. Dr. Gayatri Parthasarathy, R&D, BEL
6. Dr. Deepak P M, CEWIT, IIT Madras
7. Dr. Shaeen Kalathil, Assistant Professor, EEE, Princess Nourah Bint University, Saudi Arabia
8. Dr. Arjun Hari M, Senior Facility Technologist- Process Integration National Nanofabrication Centre, CeNSE-IISc-Bangalore

9. Dr. Nithin Abraham, Analog Circuit Design Engineer, Global Foundries

10. Dr. Haripriya Kesavan, Staff Engineer-Device TCAD at Micron Technology

NIE-ALUMNI

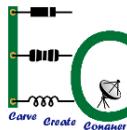
1. Mr. Charan M S, Analog Layout Engineer, Texas Instruments, Bangalore
2. Mr. Skanda Prasad, Site Reliability Engineer, Cisco Bangalore
3. Mr. Prabhanjana S K, Software Developer Engineer, Siemens Bangalore
4. Saikiran Babu Annangi, Data Science Master's Student, University of Washington
5. Mr. Santhosh H S, AI Engineer & Data Scientist @ TCS-AI Lab
6. Mr. Aniruddh Adiga, Master's in Data Science Texas A&M University
7. Ms. Sangeetha A Shayana, HVAC Engineer, Schneider Electric, Bangalore
8. Mr. Pundlik Anant Nayak, Analog Layout Engineer, Texas Instruments, Bangalore
9. Ms. Raksha A R, Analog Layout Engineer, Texas Instruments, Bangalore
10. Mr. Amith N Bharadwaj, R&D Engineer, Tejas Networks Ltd. Bangalore
11. Ms. Lekhana Gowlla, R&D, Mercedes Benz, Bangalore
12. Mr. Suhas Shastry H S, Associate Engineer, Morphing Machines, Bangalore
13. Mr. Gurusatwik Bhatta N, Project Associate, IISc Bangalore
14. Mr. G Adarsh, Associate Support Engineer Blue yonder India Pvt Ltd. Bangalore
15. Mr. Darshan Babu, Senior HW Engineer, Samsung Bangalore
16. Ms. Ananya Murali Kashyap, Masters student in Electrical engineering, University of Pennsylvania



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Events Conducted in 2024 (July to December):

- 1) Inauguration ceremony
- 2) Innovation & Design Thinking Project Expo 2024
- 3) PCB Design and arm Microcontroller Hands-on Workshop
- 4) Career Guidance Program
- 5) Orientation program
- 6) Treasure Hunt
- 7) 3D Printing Workshop for girls
- 8) Hands-on Session on Analog CMOS Circuit using Cadence Virtuoso tool
- 9) Quizmosis
- 10) Industrial Visit – Lahari Electronics
- 11) Digital Poster Design on Active and Passive Electronic Components
- 12) Analog Showdown- Analog Circuit Design Evaluation

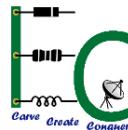


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RESEARCH WORK OF ICDC FACULTY COORDINATORS WITH STUDENTS OF IC DESIGN CLUB

1. Vas, Maelona Maxim, A. Shriranjani, B. C. Nishchith, Rajalekshmi Kishore, and Salila Hegde. "Design and Development of a Functional CANSAT Model for Atmospheric Data Collection." In 2024 IEEE Space, Aerospace and Defence Conference (SPACE), pp. 1204-1207. IEEE, 2024.
2. Remya Jayachandran, Edet Bijoy Kumaradhas, Arun S, "Design of second order low pass filter using inverter-based CMOS operational transconductance amplifier (OTA) through gm/ID Methodology", 2024 IEEE International Conference of Electron Devices Society Kolkata Chapter (EDKCON), Kolkata, India, 2024.
3. Pundlik Anant Nayak, Raksha A R, Sangeetha Shayana, Suhas Shastry, Remya Jayachandran, "A Compact and Low power memristor based 8-bit digital to Analog converter for hearing Aid devices", 10th International Conference, IEEE CONECCT July 2024
4. Remya Jayachandran, Rupesh S, Shaen Kalathil, "Intelligent Solid Waste Sorting System using IoT and Image processsing ", IEEE 2024 4th Asian Conference on Innovation in Technology (ASIANCON), Pune , Maharashtra ,India
5. Remya Jayachandran, Bhargava Dantapur, Aneeta S. Antony, and Rohini Nagapadma. "Transforming Healthcare Through Smart Health Systems: Harnessing Technology for Enhanced Patient Care." In Transforming Healthcare Sector Through Artificial Intelligence and Environmental Sustainability, pp. 107-127. Singapore: Springer Nature Singapore, 2025.
6. Rupesh, S., S. L. Aravind, B. R. Kavya, and Remya Jayachandran. "Recent advances and new concepts in methane storage and transportation." Advances and Technology Development in Greenhouse Gases: Emission, Capture and Conversion (2024): 323-354.
7. Remya Jayachandran, Nidhishree, Anush R Halappanavar, Laxman K Budni, Abhishesk C M, Charan S Hegde, and Rupesh S, "SUSTAINABLE AUTOMATED PLASTIC WASTE SEGREGATION SYSTEM USING IOT AND IMAGE PROCESSING FOR MATERIAL RECOVERY CENTERS", Second International Conference on Sustainable Technologies (ICST) 2025. (presented) - Best Paper Award 2025
8. Remya Jayachandran, Sudeep Kumar, Sanjay H, Varshini H R, Prekshadeep S P, Sangeetha A Shayana, "CMOS-Based Memristor Emulator Circuit for Audio Signal Processing in Hearing Aid Applications", In 6th IEEE International Conference on Devices for Integrated Circuits (DevIC 2025), April 2025 held at Kalyani Government Engineering College, Kolkata.