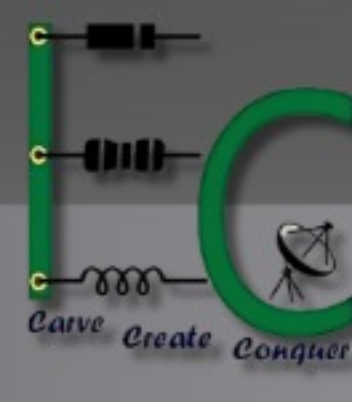




DECCTEROUS IC DESIGN CLUB IN ASSOCIATION WITH DEPARTMENT OF ECE
PRESENTS

HANDS-ON TRAINING PROGRAM

Analog IC Design Using Cadence

**27TH JAN,
2026**

**Basics of BGR circuit , Analog
Design flow in Cadence**

**28TH JAN.
2026**

**Simulation in Cadence
tool**

**29TH JAN,
2026**

Project Presentation

**30TH JAN,
2026**

**Fabrication overview, Layout
basics**

**31TH JAN,
2026**

Layout of Analog circuit

TIMINGS
10 AM to 5 PM

Resource persons:

Mr. Charan M S

Analog Layout Engineer, Texas Instruments, Bangalore

Dr. Remya Jayachandran

Assistant Professor & Deputy Head IQAC, (IEEE CASS
EXECOM Bangalore)