

Department of Electronics and Communication Engineering, NIE Mysore

The Deccterous IC Design Club, Department of Electronics and Communication Engineering, NIE Mysore, successfully organized a **five-day hands-on training program on Analog IC Design and Layout Considerations** using the **Cadence Virtuoso tool**.

The training was conducted by **Dr. Remya Jayachandran** (*Assistant Professor & Deputy Head – IQAC, IEEE CASS Execom Member*), who covered the **core concepts of Analog IC Design and Bandgap Reference Circuits** during the **first three days** of the program. The **last two days** were handled by **Mr. Charan M S** (*Analog Layout Engineer, Texas Instruments, Bangalore & Alumni of NIE*), who delivered sessions on **CMOS Fabrication, Analog Layout Techniques**, and **practical layout considerations**.

The **target audience** included **6th semester ECE students and faculty members**, with the objective of strengthening **practical understanding and industry-relevant design skills**. The program witnessed active participation from **students and faculty members** of **BGSIT, SJCE, Dayananda Sagar, MIT Mysore, and NIE Mysore**.

A total of **24 participants** successfully completed the training program. The sessions effectively combined **theoretical insights with hands-on exposure**, enabling participants to gain practical experience in **analog circuit design and layout methodologies**. Overall, the training program was **conducted successfully** and received **positive feedback** for its **technical depth, practical orientation, and industry relevance**.



Five days Hands on Training Program on Analog IC Design and Layout Considerations

organized by
Deccterous IC Design Club

in association with

Department of Electronics and Communication Engineering, NIE Mysore




Sessions		FN				AN		
Dates		10 am to 11 am	11.00 am to 11.15 am	11.15 am to 1.00 pm	1.00 PM to 1.15 PM	1.15 pm to 2.30 pm	2.30 pm to 4 pm	4 pm to 4.15 pm 4.15 pm to 4.30 PM
Day 1	27/01/2026	Introduction: Dr. Remya Jayachandran	Tea break	BGR circuit Basics: Dr. Remya Jayachandran	Evaluation	Lunch Break	Hands-on session: Introduction to Cadence Virtuoso Dr. Remya Jayachandran	Feedback , Githubrepo Update
Day 2	28/01/2026	BGR circuit design Dr. Remya Jayachandran		Hands on session: BGR Circuit Design and analysis using Cadence Virtuoso Dr. Remya Jayachandran	Evaluation		Project selection based on Research paper: Implementation of Circuit in Cadence tool Dr. Remya Jayachandran	
Day 3	29/01/2026	Hands on session Project Design Implemenation and Analysis: Dr. Remya Jayachandran		Hands on session Project Design Implemenation: Dr. Remya Jayachandran	Evaluation		Project Presentation, Githubrepo Dr. Remya Jayachandran	
Day 4	30/01/2026	Introduction to CMOS Fabrication: Mr. Charan M S, Analog layout Engineer, Texas Instruments		Layout Basics and considerations: Mr. Charan M S, Analog layout Engineer, Texas Instruments	Evaluation		Hands on session: Layout using Cadence tool Mr. Charan M S, Analog layout Engineer, Texas Instruments	
Day 5	31/01/2026	Hands-on session- Layout: Mr. Charan M S Analog layout Engineer, Texas Instruments		Project design- Layout and Post layout simulation analysis: Mr. Charan M S Analog layout Engineer, Texas Instruments	Evaluation		Project design- Layout and Post layout simulation analysis, Githubrepo, Feedback Mr. Charan M S, Analog layout Engineer, Texas Instruments	Valedictory session, Feedback





GPS Map Camera



Mysuru, Karnataka, India 

Mananthavadi Main Road, Ashokapuram, Mysuru, Karnataka
570008, India

Lat 12.282310, Long 76.640731

Saturday, 31/01/2026 05:53 PM GMT+05:30

Note : Captured by GPS Map Camera



HANDS-ON TRAINING PROGRAM

Analog IC Design Using Cadence

27TH JAN,
2026

Basics of BGR circuit , Analog
Design flow in Cadence

28TH JAN.
2026

Simulation in Cadence
tool

29TH JAN,
2026

Project Presentation

30TH JAN,
2026

Fabrication overview, Layout
basics

31TH JAN,
2026

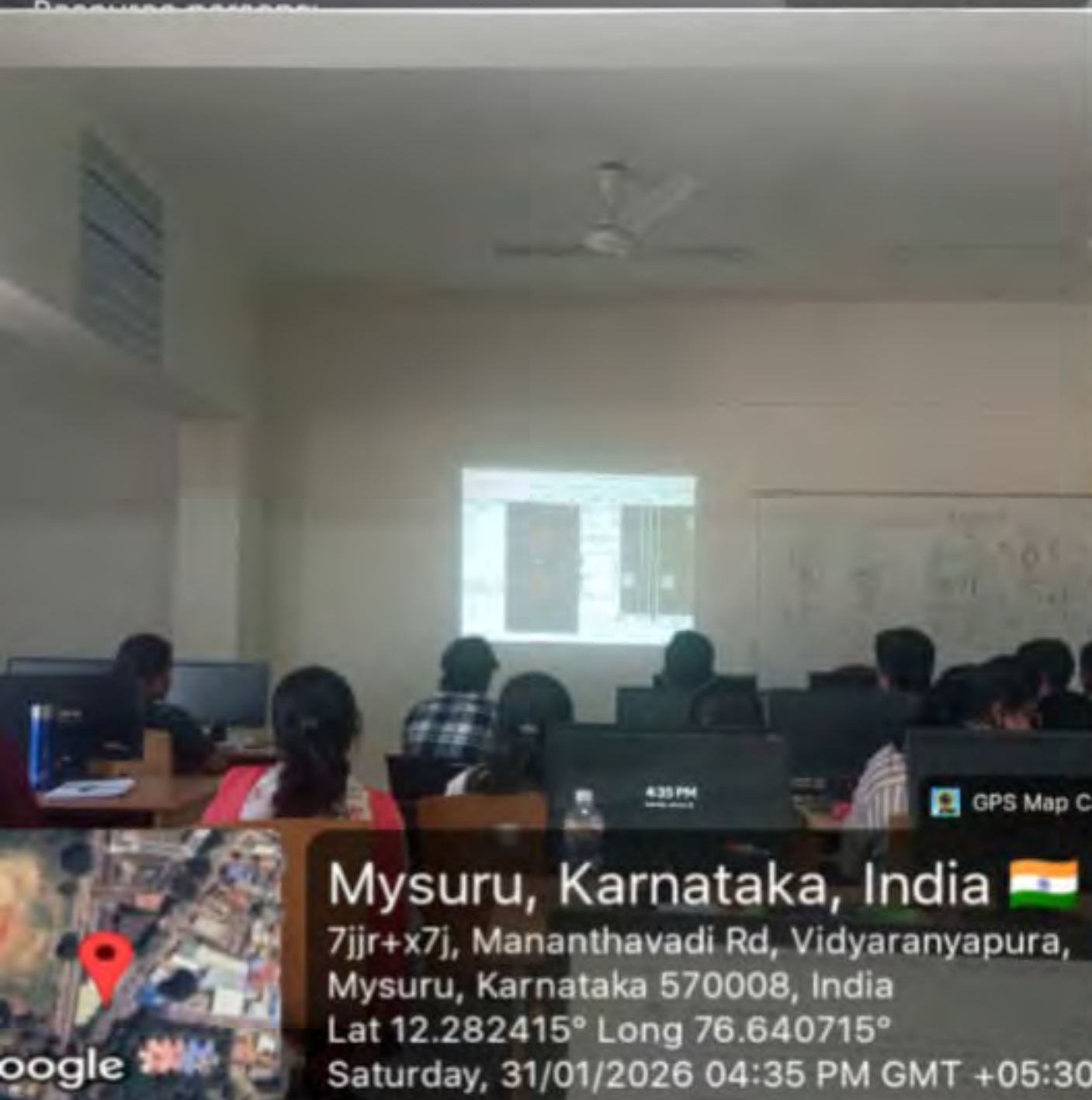
Layout of Analog circuit

TIMINGS
10 AM to 5 PM



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Mysuru, Karnataka 570008, India
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Tuesday, 27/01/2026 03:45 PM GMT +05:30



Mysuru, Karnataka, India

7jjr+x7j, Mananthavadi Rd, Vidyaranyapura,
Mysuru, Karnataka 570008, India
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Mysuru, Karnataka, India

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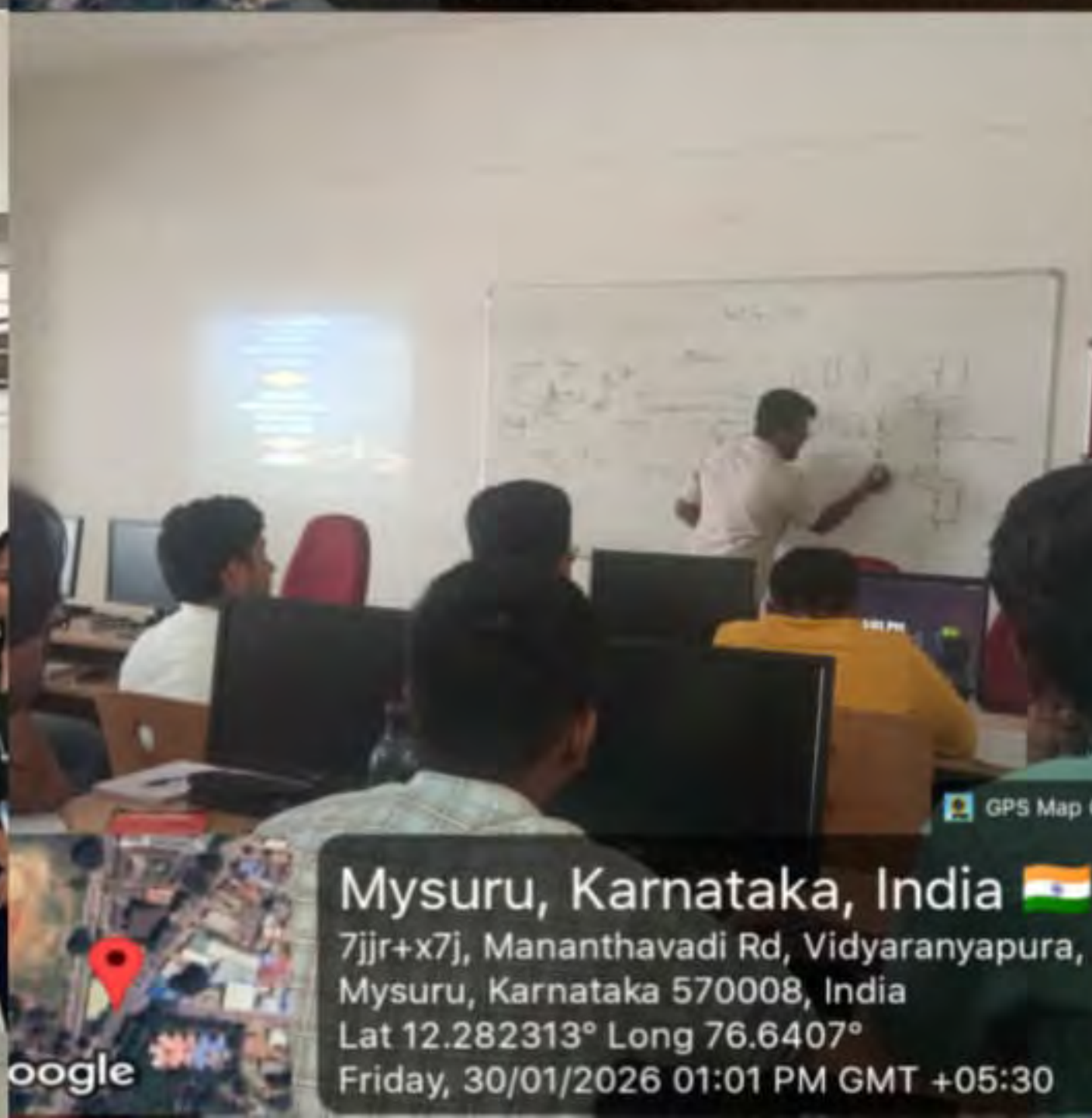
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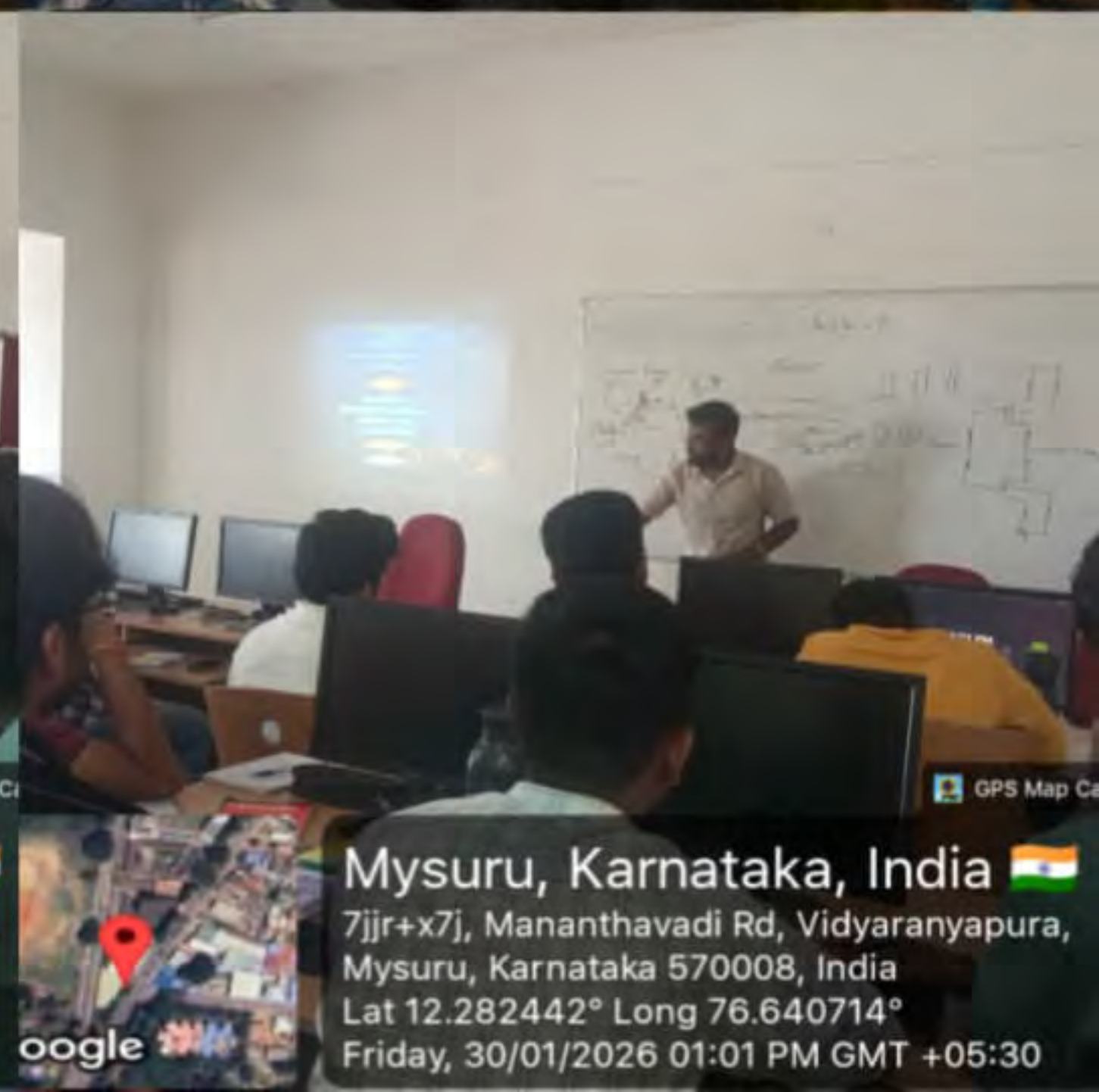
Mysuru, Karnataka, India

Mananthavadi Main Road, Ashokapuram, Mysuru, Karnataka
570008, India
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Note : Captured by GPS Map Camera



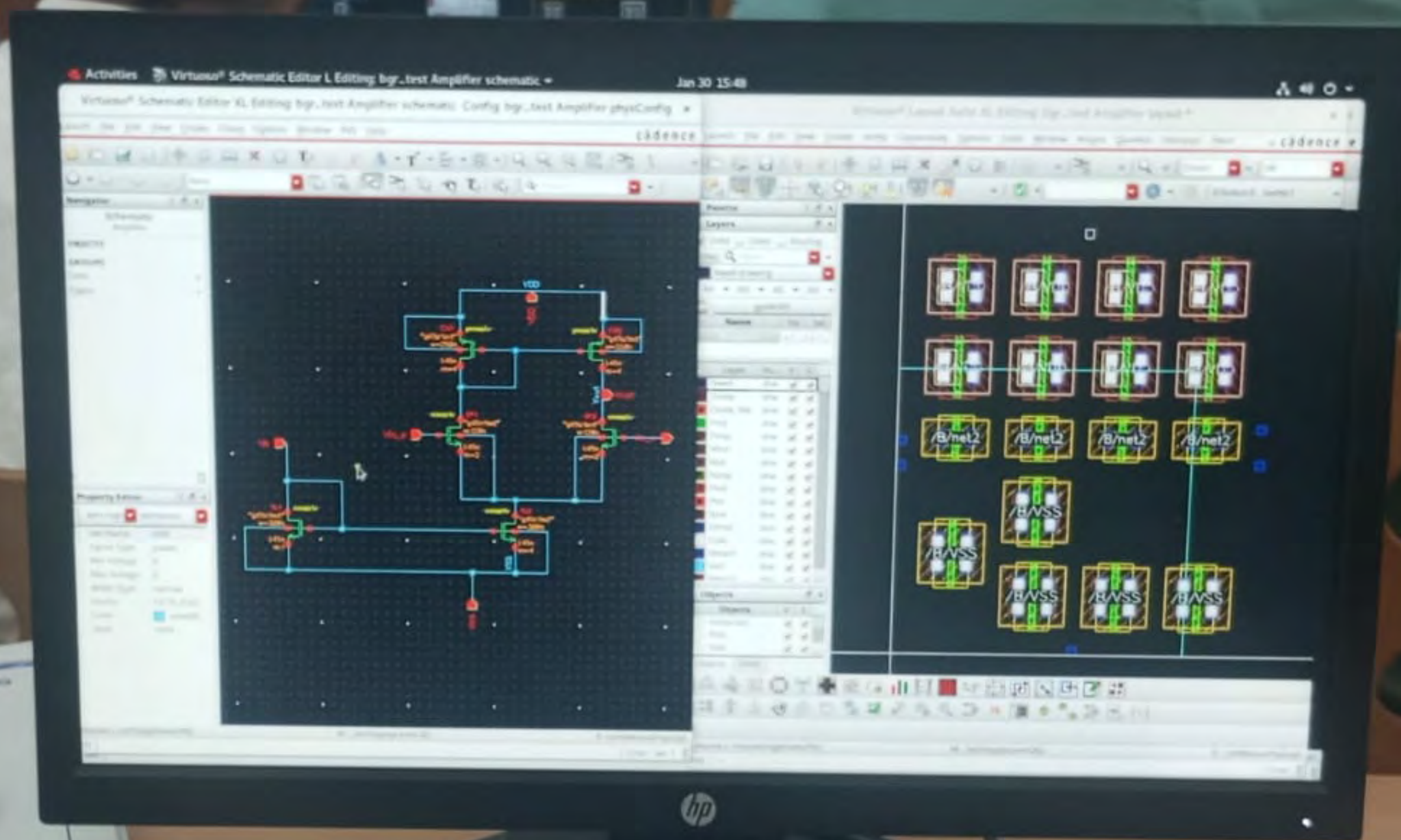
Mysuru, Karnataka, India

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Mysuru, Karnataka 570008, India
Lat 12.282313° Long 76.6407°
Friday, 30/01/2026 01:01 PM GMT +05:30



Mysuru, Karnataka, India

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Mysuru, Karnataka 570008, India
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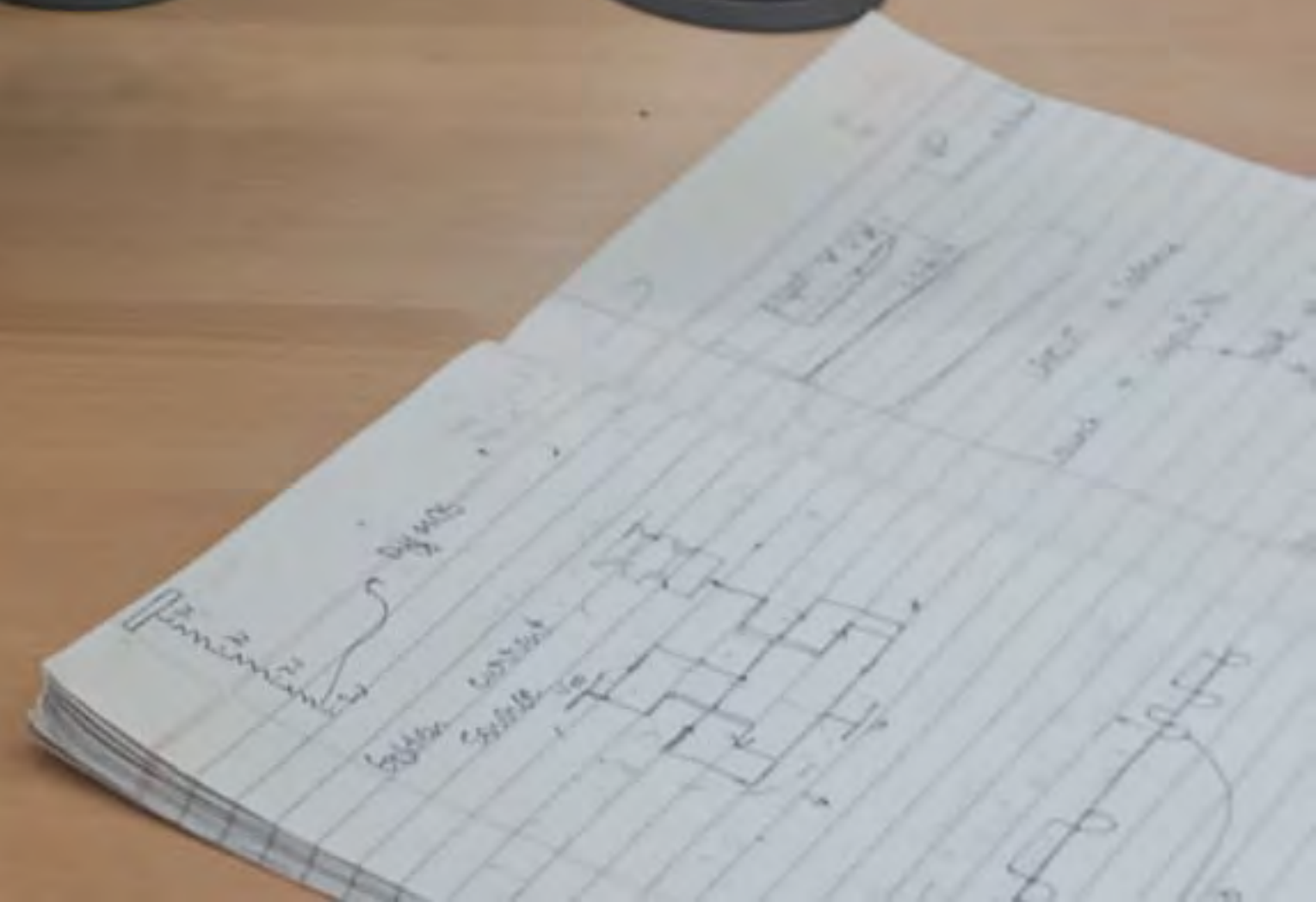
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GPS Map Camera



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Mysuru, Karnataka 570008, India


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Friday, 30/01/2026 01:01 PM GMT +05:30



GPS Map Camera



Mysuru, Karnataka, India 

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Mysuru, Karnataka 570008, India

Lat 12.282444° Long 76.640702°

Thursday, 29/01/2026 11:07 AM GMT +05:30