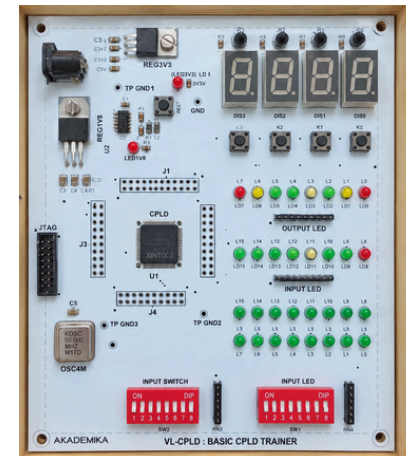


VL-CPLD CPLD Trainer Kit



FEATURES

- The VL-CPLD trainer is useful to realize and verify various digital designs
- User can construct VHDL / Verilog code and verify the results by implementing physically into the target device
- Software for CPLD: Xilinx ISE

SPECIFICATIONS

Device

- Xilinx CPLD COOL RUNNER II XC2C64A-5VQG 100 C Package

User interface

- 16 input switches with LED indication
- 16 output LEDs
- 1 x 4 matrix keyboard
- Four multiplexed 7- segment displays

CPLD configuration through

- USB port JTAG cable

Clock oscillator

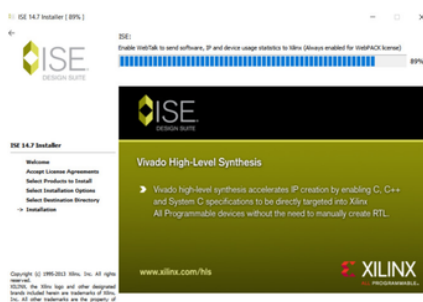
- 4MHz

Power Supply

- +5V, GND

SOFTWARE

- Xilinx ISE (Integrated Synthesis Environment) is a legacy software tool suite used for designing, synthesizing, and analyzing digital circuits for older Xilinx field-programmable gate arrays (FPGAs) and complex programmable logic devices (CPLDs).



EXPERIMENTS

- To create new project using Xilinx ISE
- VHDL Code for Half Adder
- Write a program to test Input and output
- Write a program to interface keys and seven segment display
- To test the free IOs by generating wave pattern on it.
- ALU
- DemorgansLaw
- 8:3 MUX
- 4:2 MUX
- Fulladder
- FullSubtractor
- HalfSubtractor
- LogicOperations
- UniversalGate
- D_FlipFlop
- Many and more Experiment Support.

