

Meet the Founders



■ **Brandon Lucia**
Chief Executive Officer

Brandon Lucia is the co-founder and Chief Executive Officer of Efficient Computer, where he is building the world's most energy-efficient general-purpose processor by pairing ultra-efficient hardware with developer-friendly software. His work there draws on a research career at Carnegie Mellon University, where, as the Kavčić-Moura Professor of Electrical and Computer Engineering, he founded the Abstract research group. That research exists on the boundary of computer architecture, systems, and programming languages, with a long focus on parallel and dataflow computer architectures, intermittent, energy-harvesting computer systems, and space-based computing platforms. He earned his PhD and MS at the University of Washington and his undergraduate degree at Tufts University, and his research has been recognized with a Sloan Research Fellowship, an NSF CAREER Award, and the Bell Labs Prize. At Efficient, he is turning that research into an architecture that scales from tiny physical AI sensors to the datacenter, positioning the company as the answer to computing's energy challenge.



■ **Graham Gobieski**
Chief Technology Officer

Graham Gobieski is the co-founder and Chief Technology Officer of Efficient Computer, where he leads development of processors that unlock new capabilities for the world's most energy-constrained devices. The company grew directly out of his PhD research at Carnegie Mellon, where he was co-advised by Brandon Lucia and Nathan Beckmann. His work centered on a stubborn problem: for battery-powered and energy-harvesting devices, moving data costs far more than computing on it, so longer-lived, smarter sensors depend on radically more efficient on-device computation. That research produced the spatial dataflow architectures and tools that now underpin the company. He studied Computer Science at Columbia before CMU and was named an Apple AI/ML Scholar during his doctorate. At Efficient, he is taking those ideas from research prototypes to a commercial architecture that can power medical implants, infrastructure monitors, satellites, and the next generation of physical AI.



■ **Nathan Beckmann**
Chief Architect

Nathan Beckmann is a co-founder and Chief Scientist at Efficient Computer, where he is helping rethink general-purpose computing from the ground up to make it dramatically more energy efficient. His work draws on a research career at Carnegie Mellon University, where, as Associate Professor of Computer Science, he founded the CORGi research group. That research attacked a tradeoff long thought unavoidable: that a computer must give up efficiency to stay general-purpose. Working from the insight that conventional processors waste most of their energy moving data rather than computing on it, his group built data-centric architectures and programming models that make ordinary high-level software as efficient as purpose-built hardware.. He earned his PhD and MS at MIT and his undergraduate degrees in Computer Science and Mathematics at UCLA, and his research has been recognized with a Sloan Research Fellowship, an NSF CAREER Award, and induction into the SIGMICRO Hall of Fame. At Efficient, he is turning years of work on energy-minimal architecture into real silicon that runs general-purpose software at a fraction of the power.





■ Alex Hawkinson
Co-Founder

Alex Hawkinson is the Founder and CEO of BrightAI, where he leads the mission to awaken critical infrastructure and shift operations from reactive to proactive. With over 25 years of experience across IoT, AI, SaaS, and cloud, Alex previously founded SmartThings — earning the "father of IoT" title — growing it into a global platform with 1B+ connected devices and 250,000 developers before its 2014 acquisition by Samsung. He also co-founded EfficientAI (energy-efficient computing) and OurSky (space-based technologies), serves as chairman of both, and sits on the boards of Strikepoint Group Holdings and Pelsis Group, as well as the Advisory Council for Carnegie Mellon's College of Electrical and Computer Engineering.

