

Solid State Interlocking Data Appreciation



Aim

This course will enable a designer, tester or project engineer to deduce the detailed interlocking function by reading the various data files, and to identify that mandated quality procedures have been carried out.

Key Features

- SSI data constructions and writing conventions.
- SSI data examples and comprehension exercises
- All commonly occurring data files for all parts of the SSI interlocking system are covered:
 - Interlocking identities: TCS, SIG, PTS, ROU, ELT, QST & FLG files
 - Interlocking MPM data: IPT, FOP, PFM, PRR, OPT & MAP files
 - Panel processor identities: BUT & IND files
 - Panel processor Data: PSD, PPD, PTD, POD & PBK files
 - Understanding the “Newton mod” for points keying in the above files
 - Diagnostic processor: DIA file
 - Overview of simulator and interface processor map design and co-ordinate assignment
- Overview of processes for data writing, configuration control and testing

Course Outcomes

On successful completion of this course, the delegate will be able to demonstrate how common interlocking principles and control table requirements are implemented by the data.

Assessment and Certification

The underpinning knowledge gained by the delegate will be measured by a theory assessment.

The delegate’s achievement of the Course Outcomes will be confirmed by a certificate which will be forwarded to the nominated client contact following the course.

Pre-Requisites

A well prepared delegate is expected to be able to interpret signalling plans and control tables for UK main line signalling installations, and specifically to relate trackside objects to SSI module/ telegram allocations.

Practical Information

- Duration: 5 days
- Location: At our Derby training centre, or on your premises.
- Maximum number of delegates: 12

This course is produced and run by Signet Solutions.

For further information contact us:

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Course Progressions

We offer many signalling technical courses, and it can be difficult to work out what's best for your needs. The following table will assist you.

*Development Courses in green text

These courses typically form the backbone of a career development path, and are usually taken in the order shown. *Available on an "open" basis, in which you can take individual places from our regular timetable.*

*Supplementary Courses in blue text

These courses provide supplementary knowledge about a specific technology or process. They can generally be taken on an "as needed" basis, without any particular order.

Available on a "private" basis, in which you sponsor the delivery of a full course. This works better for four or more delegates.

This is just a quick guide – please consult our individual course specifications for more detailed information. Please ask us if you have any queries.

Signal Maintenance & Signal Installation	Signal Design	Signal Works Testing
Introduction to Signalling/ Basic Signalling 1 & 2	Basic Signalling Technology Intermediate Signalling Technology Layouts Intermediate Signalling Technology Control Tables Advanced Signaling Technology	Introduction to Signalling/ Basic 1 & 2 Mod 5 - Test Assistant Mod 3c - Verification Tester Mod 3BL - Functional Tester Mod 4 - Functional Tester Mod 2 - Principles Tester Mod 1 - Tester in Charge
SMTH - Signalling Maintenance Testing Handbook Route Relay Interlocking - Maintenance + Faulting Clamp Lock Installation Clamp Lock Maintenance Cable Jointing Supplementary Back Drives + Stretcher Bars EISS Electrical Installation Skills Electrical Principles Style 63 Points Installation Style 63 Points Maintenance Westpac MK111A Maintenance + Faulting HW100 Points Maintenance EBI Track 200/T121 Track Circuits Fault Finding Techniques Mechanical Signalling	Route Relay Interlocking Appreciation Route Relay Interlocking Design Western Region E10k Circuitry Correlation Westpac MK11A - Design Location Design Project Level Crossing Design SSI Appreciation SSI Control Tables SSI Data Appreciation SSI Data Preparation Route Relay Interlocking - Mod 3BI Westpac MK11A - Testing	