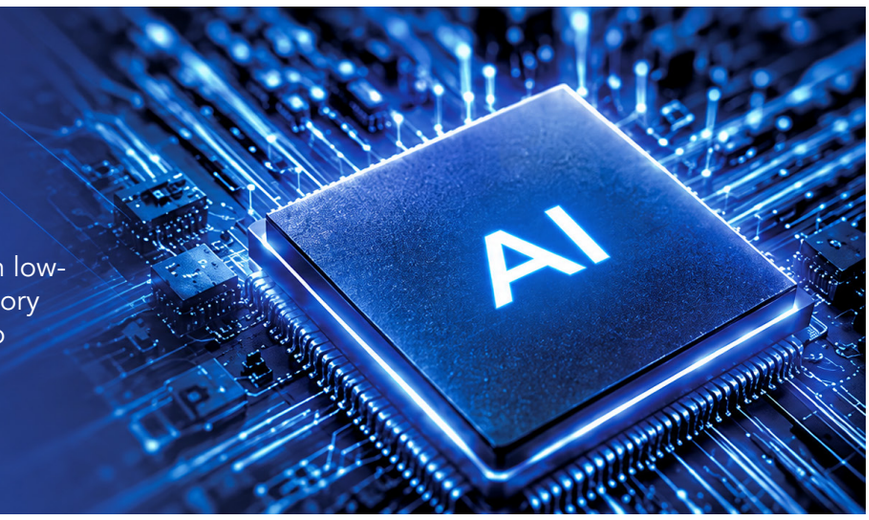
INTERA
SEAMLESS AI

INTERA Cognit

AI Accelerator IP Family

A family of ultra-low-power AI accelerators, from low-latency RISC-V custom instructions to near-memory computing engines, enabling TinyML systems to achieve maximum performance-per-watt with a minimal silicon area and memory footprint.



EAGLE

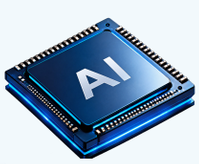
MxV
(indirect MxM)



- MxV (indirect MxM)
- 4, 16 PEs (testing up to 32 banks/PEs)
- Configurable 2 to 32-bit
- Maximum performance per watt for sensor-proximal AI

FALCON

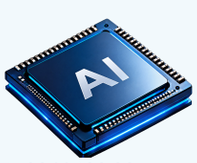
MxV
(indirect MxM)



- MxV
- Single PE
- 2 to 32-bit implementation
- Ultra-low-power matrix-vector accelerator

HAWK Advance

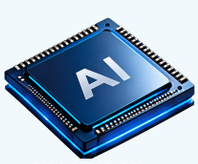
MxV
(indirect MxM)



- MxV / MxM (indirect via software)
- Single PE
- 2 to 32-bit implementation
- Entry-level version, scales to Hawk Pro

HAWK Pro

MxV
(indirect MxM)



- MxV / MxM (indirect via software)
- 1, 2 or 4 PEs
- Configurable 2 to 32-bit
- High flexibility and scalability

HARRIER

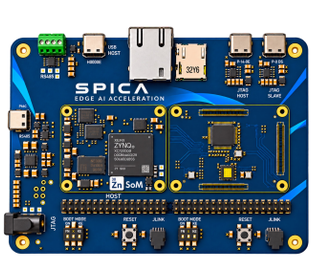
MxV
(indirect MxM)



- MxM
- Scalable matrix array 8 x 8 = 64 PEs
- Configurable as 32x32, 32x64, 64x64, ...
- For high-throughput Edge AI

Typical target applications

- Health-monitoring wearables
- Wake-word detection
- Acoustic event detection (AED)
- Neuromorphic vision
- Occupancy sensing
- Predictive maintenance
- Fall detection
- Radar perception
- Smart security sensors
- BMS (state of charge)



TinyML
BIG POSSIBILITIES IN A SMALL FOOTPRINT

Edge AI
INTELLIGENCE CLOSER TO THE REAL WORLD

Low-Power
MAXIMUM PERFORMANCE-PER-WATT

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